

ABSTRACT

In the present invention, during a 1H period excluding a blanking period (1HB) constituting a line display period, pixel data pulses of RGB (61B to 61R) are successively
5 supplied for each color to corresponding signal lines for color display of one pixel line. A control circuit (40) of select switches connected to the signal lines (6-1 to 6-n) supplies permission pulses (63B to 63R) for supply of data to signal lines when displaying one color among RGB to
10 select switches (TMG), and turns on the select switch (TMG) of the signal line corresponding to another color to be displayed later in the same line display period during the period of this application by a precharge pulse (62G or 62R) having a time duration shorter than the supply time of
15 the pixel data of the other color (T2 or T3) to previously precharge the signal line of the other color to the predetermined potential. Due to this, sufficient precharging of a signal line which became difficult due to an increase of a load capacitance of the signal line due to
20 higher definition of the image display device and increase in speed of a drive clock thereof can be achieved.